

**ABSTRACT**

A method of forming an isolation layer in the semiconductor devices is disclosed. The ~~present invention method comprises~~ includes forming the isolating film by means of a method in which a method of forming a ~~V-type~~ V-type trench at the isolation region, implanting ions capable of accelerating oxidization action into the center portion of the ~~V-type~~ V-type trench, implementing an oxidization process to form an insulating film consisting of an oxide film at the isolation region, and then completely burying the trench with an insulating material, using the LOCOS method, and a method of forming a trench type isolation layer, are applied together. Therefore, as the top corner of the trench is formed with an inclination, and a concentration of the electric field and a formation of a moat can be simultaneously prevented.